

**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****In re Application of:**

Culler et al

Serial No.: 10/619,909**Filed:** July 14, 2003**For:** OSCILLATOR METHOD AND
APPARTUS FOR A TEST CHIP**Confirmation No.:** 7182**Examiner:** J. Chang**Group Art Unit:** 2817**Attorney Docket No.:** 200206685-1**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being facsimile transmitted to the USPTO (703) 746-4000 on the date indicated below

1/12/05
DateShannan Harding
SignatureShannan Harding
Name (Type/Print)**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop Issue Fee
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The Examiner indicates:

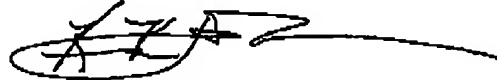
None of cited references discloses nor suggests the claimed invention including a delay circuit operably coupled into the ring oscillator configuration modifies the predetermined frequency of the ring oscillator configuration, which the coupling occurs on a semiconductor package containing the semiconductor device or a circuit board containing the semiconductor device, as set forth in the claims.

Applicants concur with the reasons as stated by the Examiner insofar as they comprise a summary, and are exemplary and not limiting. However, the independent claims as allowed include other and different language than that specified by the Examiner, and the allowed dependent claims include other and further features and elements. Accordingly, the scope of

Serial No. 10/619,909

the claims must be determined from the literal language of each as a whole, as well as equivalents thereof.

Respectfully submitted,



Kevin K. Johanson
Registration No. 38,506
Attorney for Applicants
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

KKJ/dlm

Date: January 5, 2005.